TO PLB ON SG900 — PLA

~1V	1	→ PLB/1
FREQ. IN	A	
−10V	2	→ PLB/2
LINE PULSE	B	→ PLB/B
~1V	3	→ PLB/3
+6V	C	→ PLB/C
PULSE IN	4	
−6V	D	→ PLB/D
	5	
+10V	E	→ PLB/E
	6	
0V	F	(X14,X15,X16,PIN 8; PLB/F:X11,X20,PIN 2 X1,X3,X4,X6,X10,X12,X18,X8,X7,PIN 10
+7·5V	7	→ PLB/7
LINE PULSE	H	→ PLB/H
	8	
FIELD PULSE	J	→ PLB/J

MIXED BLANKING OUT — SKT A (STUDIO VERSION)

MIXED SYNC OUT — SKT B (STUDIO VERSION)

PLB — TO PLA ON C.C.U.

PLA/1	1	1V
	A	MIXED SYNC OUT
PLA/2	2	−10 V
PLA/B	B	LINE PULSE
PLA/3	3	1V
PLA/C	C	+6V
	4	
PLA/D	D	−6V
	5	
PLA/E	E	+10V
	6	MIXED BLANKING OUT
PLA/F	F	0V
PLA/7	7	+7·5V
PLA/H	H	DELAYED LINE PULSE
	8	
PLA/J	J	FIELD PULSE

SKT C — LINE DRIVE OUT (STUDIO VERSION)

SKT D — FIELD DRIVE OUT (STUDIO VERSION)

LK 2: AH, AE, AD

LINE BLANKING GEN. X1 — GATES 1 & 2 X2 — CLIP X3 (as X6) — LINE SYNC PULSE GENERATOR X4 — GATES 7 & 10 X5 — CLIP X6 — FIELD SYNC PULSE GEN. X7 — CLIP X8 (as X6) — EQUALIZING PULSE GEN. X9 — DELAY X10

NOTE:- AS X2 BUT WITH VT1-C CONNECTED TO −6V

STUDIO VERSION (BSS900/1 ONLY)

SAa, SAb, SAc, SAd, SAe, SAf, SAg, SAh — 525 / 405 / 625

GATES 4&5 X13 — ÷5 OR ÷6 (X14,X15,X16) X14 — X15 — X16 (as X15) — GATES 6 & 9 X17 — CLIP X18 (as X6) — GATES 3 & 8 X19 — FLIP-FLOP X20 (as X11)

NOTE:- AS X14 BUT WITH MR2 AND MR3 REVERSED

NOTE:- AS X2 BUT WITH VT1-C CONNECTED TO −6V

NOTES. 1. X5, X13 & X17 ARE IDENTICAL ALTHOUGH INPUT AND OUTPUT POINTS MAY VARY ON THIS CIRCUIT.
2. X3, X6, X8 & X18 ARE IDENTICAL : X15 & X16 ARE IDENTICAL: X20 & X11 ARE IDENTICAL.
3. PL A & PL B ARE EDGE CONNECTIONS ON PRINTED BOARD
4. * DENOTES UNUSED TERMINATION.

BROADCAST SYNC. SHAPER BSS900 AND BSS900/1 : CIRCUIT

.1578

FIG. 25